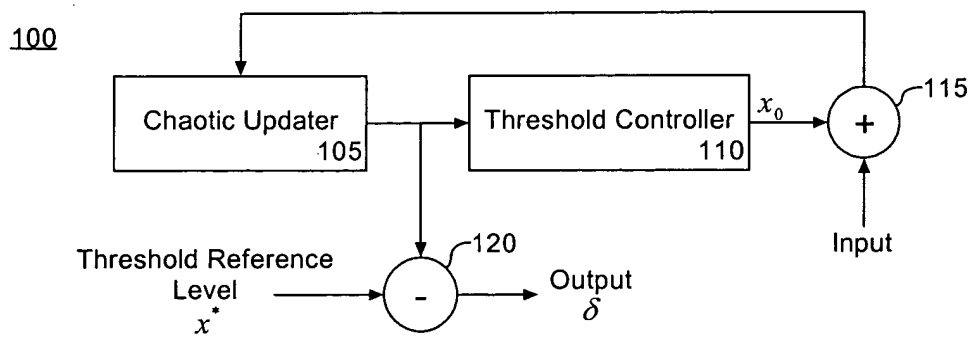


FIG. 1

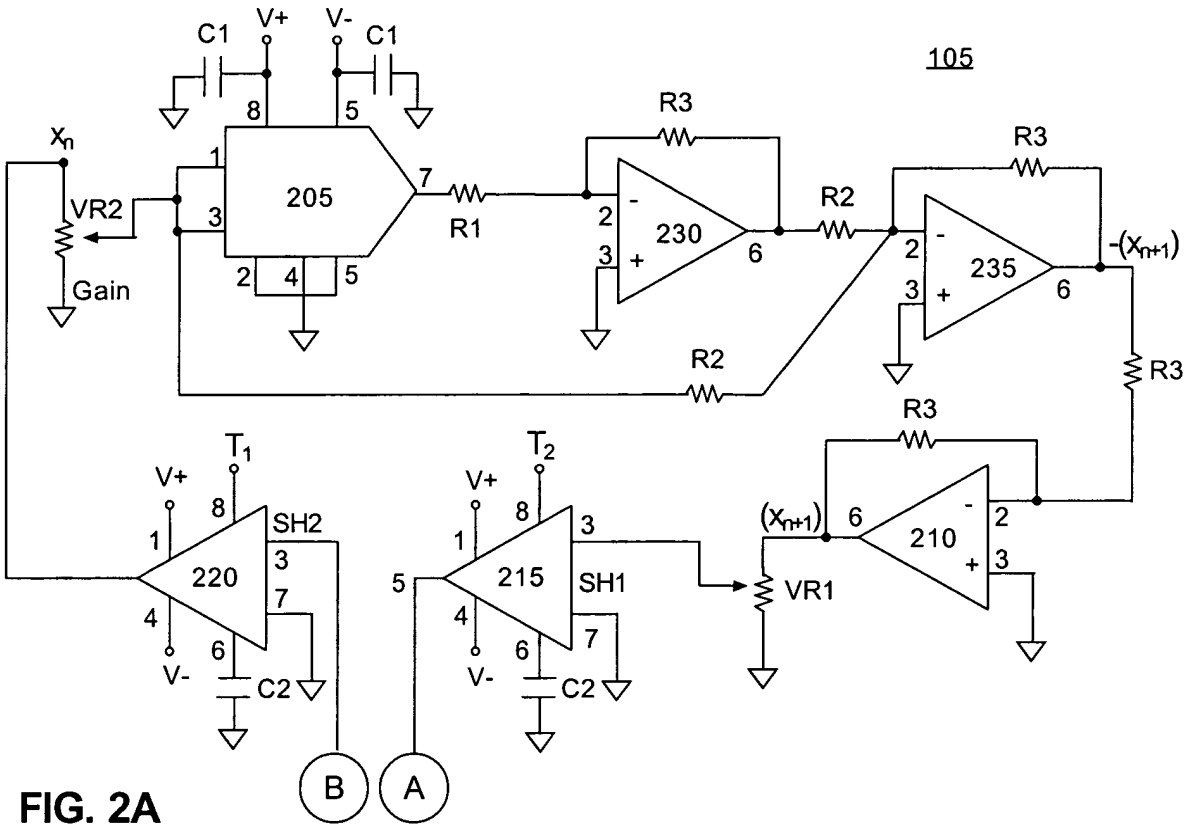


FIG. 2A

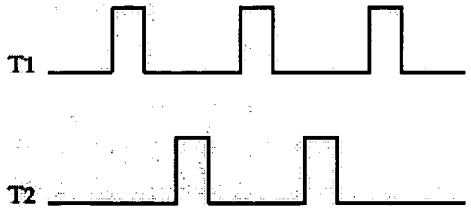


FIG. 2B

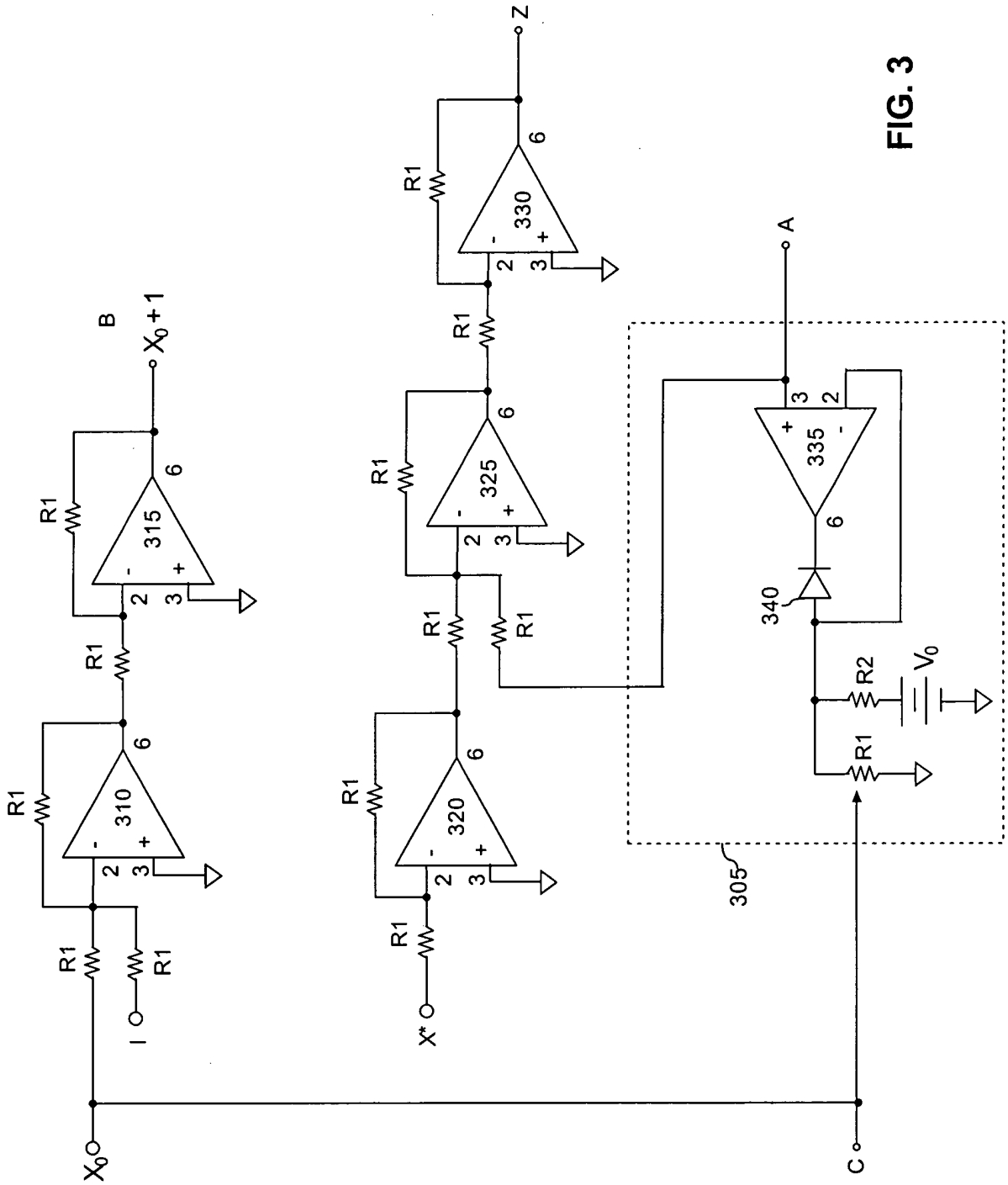


FIG. 3

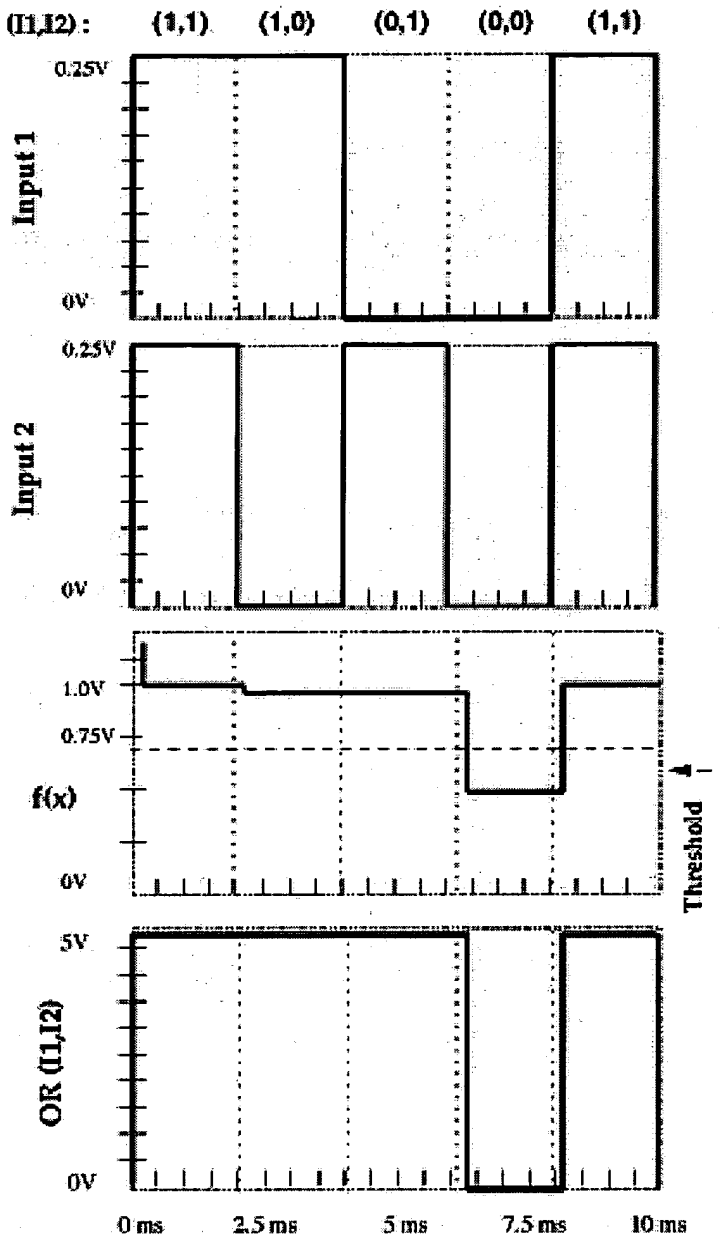

(I1,I2) :
(1,1)
(1,0)
(0,1)
(0,0)
(1,1)
0.25V
Input 1
0V
0.25V
Input 2
0V
1.0V
0.75V
f(x)
0V
Threshold
5V
OR (I1,I2)
0V
0 ms
2.5 ms
5 ms
7.5 ms
10 ms

FIG. 4A

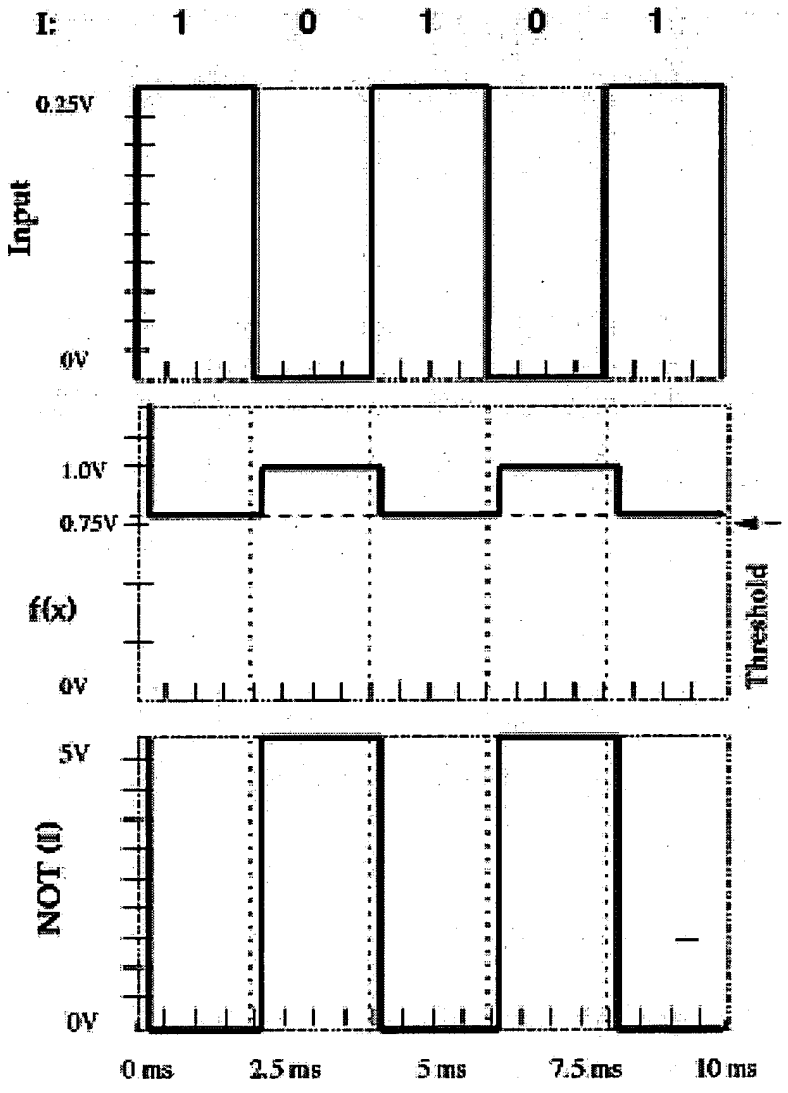

I:
1
0
1
0
1
0.25V
Input
0V
1.0V
0.75V
f(x)
Threshold
0V
5V
NOT (I)
0V
0 ms
2.5 ms
5 ms
7.5 ms
10 ms

FIG. 4B